

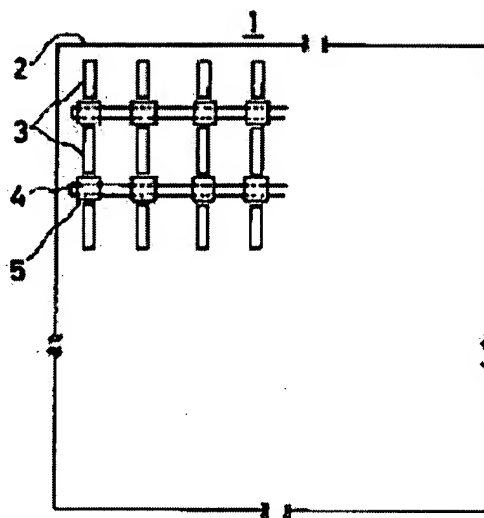
MATRIX DISPLAY BOARD

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Classification:
- **International:** H01L33/00
- **European:**
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Abstract of JP5315653

PURPOSE: To provide a matrix display board which improves reliability on connection and reduces the cost and besides is excellent in heat radiation property by using a matrix circuit board without a through hole.

CONSTITUTION: This is a matrix display board where anode circuits 3 and 4 and cathode circuits 4 and 3 are formed in orthogonal directions and besides, with either or both of them cut at the crossing of both, at one side of the printed board 2 and the cut section is connected by the three-dimensional crossing using the metallic frame of a light emitting diode lamp.



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